

Atria Institute of Technology, Bengaluru, Karnataka, India



5 DAY'S ONLINE FACULTY DEVELOPMENT PROGRAM ON CURRENT RESEARCH TRENDS IN VLSI DESIGN AND DEVICE MODELLING

Organized by

Department of
Electronics and Communication Engineering

In association with IEEE & IETE ATRIA
Student Branch Chapter

FDP on 24th-28th August-2021

About FDP

This FDP aims at providing a forum for members of the research and academic community to give awareness on analog and digital VLSI circuit design, device modeling and simulation, and basic concepts of power optimization in VLSI. Each session is enriched with well-experienced resource person who has a broad skill set in mixed mode VLSI design and Device modeling areas.

For Whom

- Interested faculties involved in research
- Industry persons
- Research scholars

Convener

Dr. T.N Sreenivasa
Principal, AIT, Bengaluru, India

Organizers

Dr. Shipra Upadhyay,
AMIETE, Associate Professor,
Dept. of ECE, AIT Bengaluru, India

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Dr. Shipra Upadhyay, Associate Professor, AIT-Bangalore
Dr. Amit Kumar Pandey, Assistant Professor, REC, Ambedkar Nagar, Uttar Pradesh
Dr. T. Sharan, Assistant Professor, NEIRST, Arunachal Pradesh
Dr. Balraj Singh, Assistant Professor, GBP Institute
Mr. Amit Saini, Director of Cadre Design Systems
Mr. Sagar, Entuple Technologies, Bangalore
Dr. Vibhu Srivastava, Assistant Professor, Reva University, Bangalore

Atria Institute of Technology, Bengaluru

Atria Institute of Technology (AIT) has made a name for itself as an institute of repute with a track record of excellent placements.

- Approved by All India Council of Technical Education (AICTE), New Delhi
- Affiliated to VTU, Karnataka
- Accredited by NAAC

Department of Electronics & Communication

The department of Electronics and Communication Engineering was established in the year 2000. The department has well-qualified faculty members and it offers subjects relevant to current industrial needs. Our faculty brings state-of-the-art research, development and design experience into the classroom.



Registration

Online Registration should be done by filling link
<https://bit.ly/3AV8pe1>

Key Features

- No Registration Fee for Faculty members
- E-Certificate will be given to all participants



Contact Us

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Schedule

- **Day 1:** Research Aspects in Low power circuit design.
- **Day 2:** Basic concept of semiconductor device modelling and its research aspects
- **Day 3:** Hands on session on Visual TCAD
- **Day 4:** Design issues of low voltage V to I converter (OTAs).
- **Day 5:** Domino logic circuits