

FACULTY PROFILE			
1.	Name	Shalini Vashishtha	
2.	Date of Birth (dd/mm/yyyy)	08/03/1972	
3.	UID (Aadhar No.)	#### #### 8548	
4.	e-Mail ID (Official)	Shalini.v@atria.edu	
5.	Designation	Asst. Professor	
6.	Department	E&C	
7.	Google Scholar Link:	https://scholar.google.com/citations?user=z18R9KwAAAAJ&hl=en&oi=sra	
8.	Area of Specialization	Computer Applications in industrial drives (Power Electronics and MP)	
9.	Courses Taught (UG)	Power electronics, OS, Basic Electronics, Basic Electrical, Digital system design. ARM and embedded systems	
10.	Courses Taught (PG)	ARM and embedded systems	
11.	QUALIFICATION DETAILS:	BE(EEE),MTech(CAID),Ph.D.(pursuing FPGA based SVPWM)	
12.	Total Experience in Years	Teaching	Industry
		20	5(Ph.D)
13.	Publication Details	<i>Type/Level</i>	National
		Journal	5
		Conference	
14.	No. of Research and Projects Guided	9(UG)	
15.	No. of Books Published/IPRs/Patents		
16.	Sponsored Projects/Grants (if any)		
17.	Professional Memberships	ISTE(Indian Society of technical education)	
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.	Alumni, NAAC, NBA	
19.	Academic Achievements	BE(Honors) ,MTech(FCD with Distinction)	
20.	Extra-Curricular Achievements		
21.	Co-Curricular Achievements		
22.	No. of Workshops/Seminars Conducted	3	
23.	No. of Workshops/Seminars Attended	15	
24.	Sports Achievements		
25.	Awards and Recognition (If any)	Recipient of Merit Scholarship (HRD Ministry class 10 and 12), GATE Scholarship	