

FACULTY PROFILE						
1.	Name	Dr. Shalabh Kumar Mishra				
2.	Date of Birth (dd/mm/yyyy)	14/06/1988				
3.	UID (Aadhar No.)	696469834981				
4.	e-Mail ID (Official)	Shalabh.km@atria.edu , shalabhmishra14@gmail.com				
5.	Designation	Research Assistant Professor				
6.	Department	ECE				
7.	Google Scholar Link:	https://scholar.google.com/citations?hl=en&user=mCeMVZQAAAAJ				
8.	Area of Specialization	Signal Processing, DSP, fractional-order Systems				
9.	Courses Taught (UG)	DSP, Signal & Systems, Analog VLSI Design, Electromagnetic Field Theory, Digital Image Processing, Engineering Statistics and Linear algebra, Electronics and Embedded System, VHDL.				
10.	Courses Taught (PG)	-				
11.	QUALIFICATION DETAILS:	M.Tech, UGC-NET (Eligibility for AP), PhD				
12.	Total Experience in Years	Teaching	Industry	Research		
		2	0	3.3		
13.	Publication Details	<i>Type/Level</i>	National		International	
		Journal	0	7		
		Conference	0	5		
14.	No. of Research and Projects Guided	10				
15.	No. of Books Published/IPRs/Patents	0				
16.	Sponsored Projects/Grants (if any)	0				
17.	Professional Memberships	Life Time Member Vijnana Baharati (VIBHA)				
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.	NAAC, NBA				
19.	Academic Achievements	Qualified GATE in 2013 (99.07 percentile) & in 2017. UGC-NET JRF				
20.	Extra-Curricular Achievements					
21.	Co-Curricular Achievements	Worked as Teaching / Research Assistant during my M.Tech				
22.	No. of Workshops/Seminars Conducted	0				
23.	No. of Workshops/Seminars Attended	12				
24.	Sports Achievements	Member of Cricket Team NSIT Faculty-11 (2018-19)				
25.	Awards and Recognition (If any)	Reviewed Several reputed research papers and book chapters including IEEE Transaction (TCSII)				