

FACULTY PROFILE			
1.	Name		Amulya D Raj
2.	Date of Birth		17/10/1991
3.	UID (Aadhar No.)		#####5744
4.	e-Mail ID (Official)		amulya.dr@atria.edu
5.	Designation		Assistant Professor
6.	Department		Electronics and Communication Engineering
7.	Google Scholar Link:		https://scholar.google.com/citations?user=K1HZsXIAAAAJ&hl=en&authuser=1
8.	Area of Specialization		VLSI Design and Embedded System
9.	Courses Taught (UG)		<ul style="list-style-type: none"> • Basic Electronics • Digital System Design Using Verilog • Fiber Optics Networks • Power Electronics
10.	Courses Taught (PG)		Nil
11.	QUALIFICATION DETAILS:		M. Tech. in VLSI Design and Embedded System B. E. in Electronics and Communication Engineering
12.	Total Experience in Years		Teaching
			Industry
			Research
			1
13.	Publication Details	<i>Type/Level</i>	National
		Journal	
		Conference	1
			International
14.	No. of Research and Projects Guided		Project Guided: 4
15.	No. of Books Published/IPRs/Patents		Nil
16.	Sponsored Projects/Grants (if any)		Nil
17.	Professional Memberships		Nil
18.	Coordinator/In-Charge/Part, of NAAC/NBA/any other Accreditations.		<ul style="list-style-type: none"> • Internship coordinator • ERP coordinator • Part of NBA affiliation work
19.	Academic Achievements		Nil
20.	Extra-Curricular Achievements		Nil
21.	Co-Curricular Achievements		Nil
22.	No. of Workshops/Seminars Conducted		Conducted one day workshop based on Verilog
23.	No. of Workshops/Seminars Attended		5
24.	Sports Achievements		Nil
25.	Awards and Recognition (If any)		Nil