



September 19, 2018

3 Day FDP on Mentor Graphics Tools

A three day faculty development programme on “Analog and Digital IC design flow using Mentor Graphics Tools” started on 19 September 2018 at 9.30 AM.

The FDP is organized by Department of ECE in collaboration with Coreel Technologies. The goal of the FDP is to provide awareness to faculty about industry standard analog and digital IC design tools. Trainer Mr. Vijendra explained the complete full custom design flow. The hand on training was provided on tools such as Questa, Leonardo Spectrum, S-Edit, T-Edit etc.

The FDP will be covering both ASIC and full custom design flow using different tools.